

FIG. 3

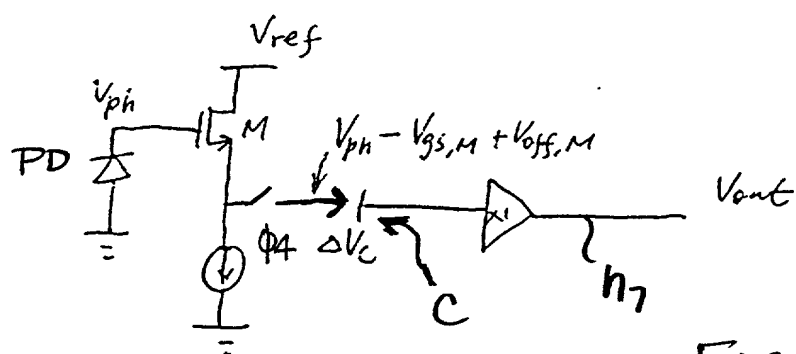


FIG. 4

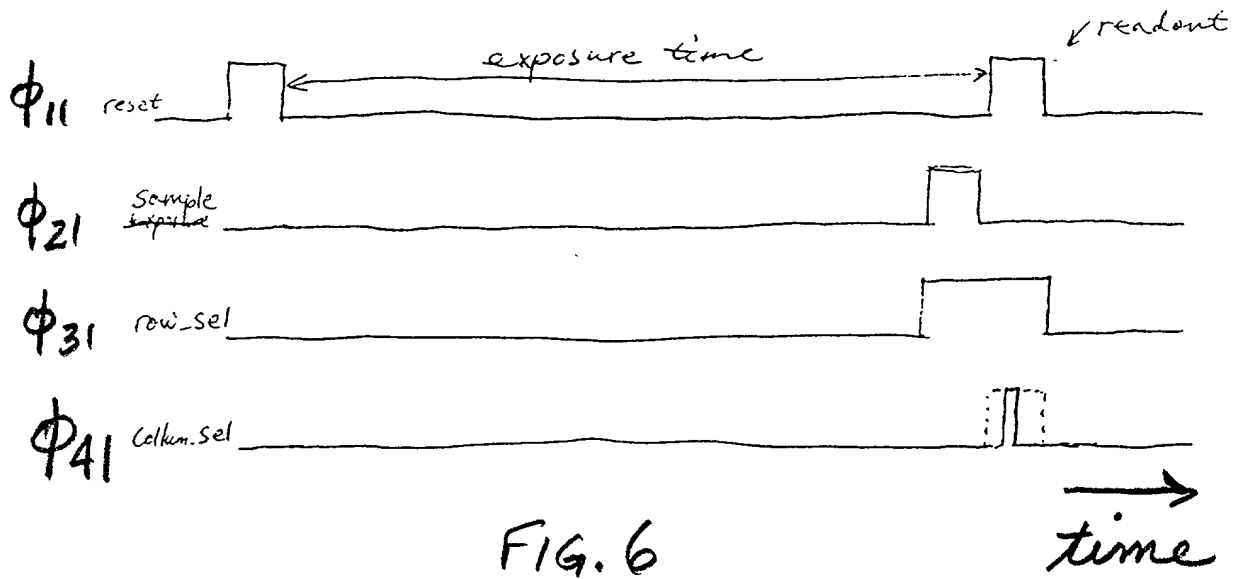


FIG. 6

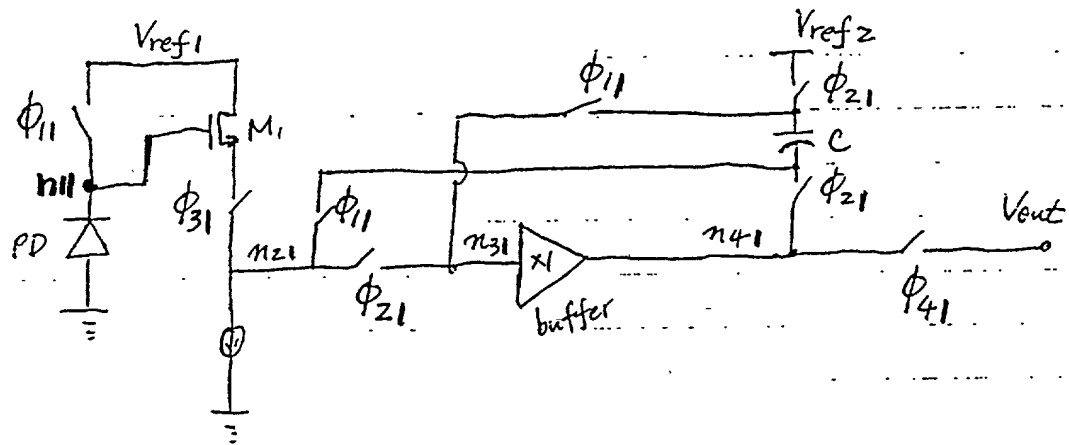


FIG. 5



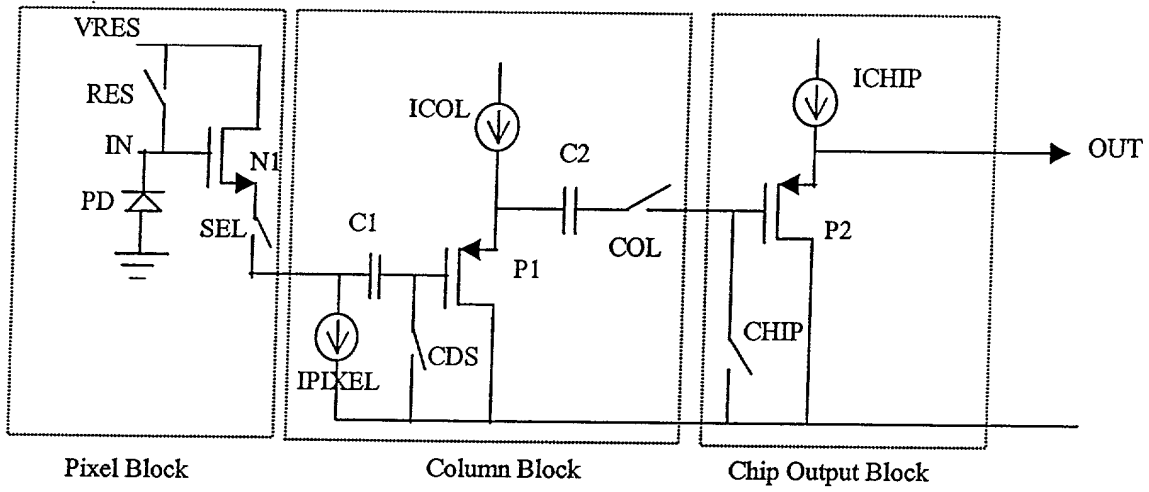


FIG. 9

	RES	SEL	CDS	COL	CHIP
<b>Integration Period</b>	OPEN	OPEN	X	X	X
<b>Readout Step 1</b>	OPEN	OPEN	CLOSED	CLOSED	CLOSED
<b>Readout Step 2</b>	OPEN	CLOSED	CLOSED	OPEN	CLOSED
<b>Readout Step 3</b>	OPEN	CLOSED	OPEN	OPEN	OPEN
<b>Readout Step 4</b>	CLOSED	CLOSED	OPEN	CLOSED	OPEN
<b>Readout Step 5</b>	OPEN	OPEN	OPEN	OPEN	CLOSED
<b>Ready for next Pixel readout</b>					

FIG. 10